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	Application Number		10565468		
	Filing Date		2006-01-19		
INFORMATION DISCLOSURE	First Named Inventor	Qinglia	ang Liu		
STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Art Unit		2416		
(Not for submission under 37 CFR 1.99)	Examiner Name	Maxwell A. Clark			
	Attorney Docket Number		r 80170-1010		

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Receipt date: 02/25/2009 10565468 **Application Number** 2006-01-19 Filing Date INFORMATION DISCLOSURE First Named Inventor Qingliang Liu STATEMENT BY APPLICANT Art Unit 2416 (Not for submission under 37 CFR 1.99) Maxwell A. Clark **Examiner Name** Attorney Docket Number 80170-1010 FAN YAXI ET AL, "Design and FPGA Implementation of Ethernet over Multiple E1 Links", Application of Electronic 1 Techniques, vol. II, 2002, pages 72-74. If you wish to add additional non-patent literature document citation information please click the Add button **EXAMINER SIGNATURE** 06/03/2009 /Brian Nguyen/ **Date Considered Examiner Signature** *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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